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<u>L14</u>	key with address	21815	<u>L14</u>
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<u>L7</u>	12 with 15	416	<u>L7</u>
<u>L6</u>	12 with 14	74	<u>L6</u>
<u>L5</u>	partition\$	411042	<u>L5</u>
<u>L4</u>	memory adj2 (map or table)	25508	<u>L4</u>
<u>L3</u>	rout\$	753516	<u>L3</u>
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1 An architecture based on the memory mapped node addressing in reconfigurable interconnection network

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